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Attorney Docket No.:RAB 2001-1

UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: *Belgard, Richard A.*
Serial No.: 09/757,439
Filed: 1/10/01 as a continuation of application
serial no. 08/458,479 filed on June 2, 1995, now
U.S. Patent No. 5,895,503 and a continuation of
application serial no. 09/905,410 now U.S. Patent
No. 5,960,466 filed August 4, 1997. The present
application is also a continuation of serial no.
08/905,356 filed August 4, 1997.
For: *Speculative address translation for processor
using segmentation and optional paging*

) Art Unit: 2185
)
)
) Examiner: *Unknown*

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INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

1. Pursuant to 37 C.F.R. 1.97 and 1.98, and in compliance with 37 C.F.R. 1.56, the Office's attention is directed to the patents, publications and other information listed on the attached PTO-1449. Regarding the document(s), publication(s) or other information listed on the attached PTO-1449, Applicant believes the same may qualify as "prior" art to this application and should be treated accordingly, although Applicant(s) reserve(s) the right to contest the prior art status of any document, publication or information cited herein.

2. Pursuant to 37 C.F.R. 1.97(b) this Statement is being filed within 3 months of the filing date.

3. Copies of the aforementioned references were provided in the parent application to the above, except for the following reference which is included herein: Hua, A. Et al., "Early Resolution of Address Translation in Cache Design," Proc. of Int'l Conf. on Computer Designs, Oct. 1990, pp. 408-412. Should it be necessary for the applicant to provide additional copies, please contact the undersigned.



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Respectfully submitted,

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I hereby certify that the foregoing is being deposited with the U.S. Postal Service, postage prepaid, to the Commissioner of Patents and Trademarks, this 10th day of April 2001.